

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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## HIGH ISOLATION VOLTAGE SINGLE TRANSISTOR TYPE MULTI PHOTOCOUPLER SERIES

–NEPOC Series–

### DESCRIPTION

The PS2501-1, -4 and PS2501L-1, -4 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon phototransistor.

The PS2501-1, -4 are in a plastic DIP (Dual In-line Package) and the PS2501L-1, -4 are lead bending type (Gull-wing) for surface mount.

### FEATURES

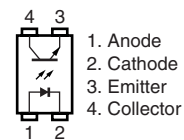
- High isolation voltage ( $BV = 5\,000\text{ Vr.m.s.}$ )
- High collector to emitter voltage ( $V_{CE0} = 80\text{ V}$ )
- High-speed switching ( $t_r = 3\ \mu\text{s TYP.}$ ,  $t_f = 5\ \mu\text{s TYP.}$ )
- Ordering number of tape product: PS2501L-1-F3: 2 000 pcs/reel
- Safety standards
  - UL approved: No. E72422

### APPLICATIONS

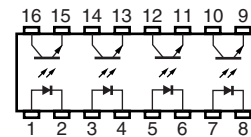
- Power supply
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controller

### PIN CONNECTION (Top View)

#### PS2501-1, PS2501L-1



#### PS2501-4, PS2501L-4

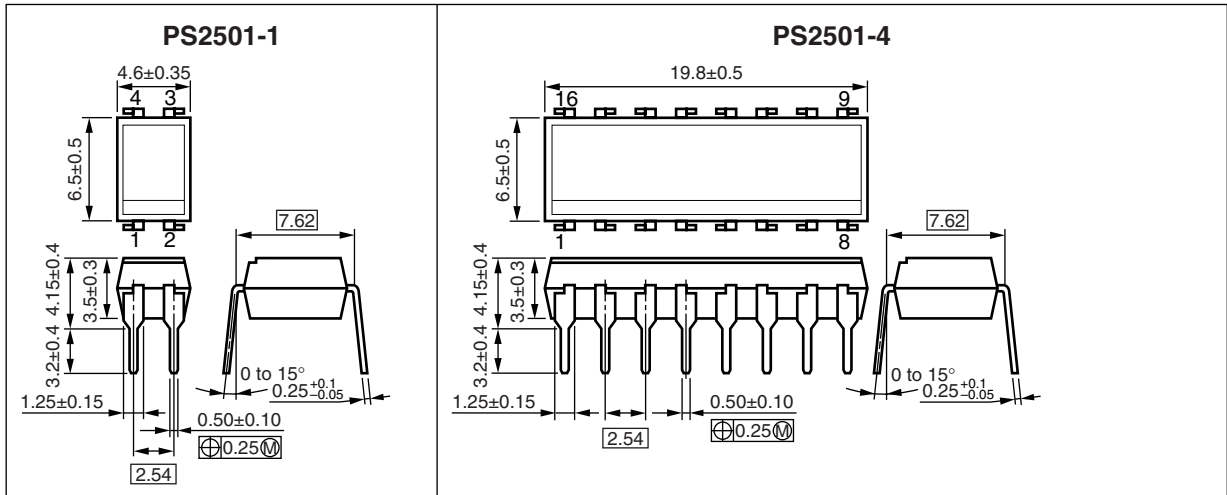


- 1, 3, 5, 7. Anode
- 2, 4, 6, 8. Cathode
- 9, 11, 13, 15. Emitter
- 10, 12, 14, 16. Collector

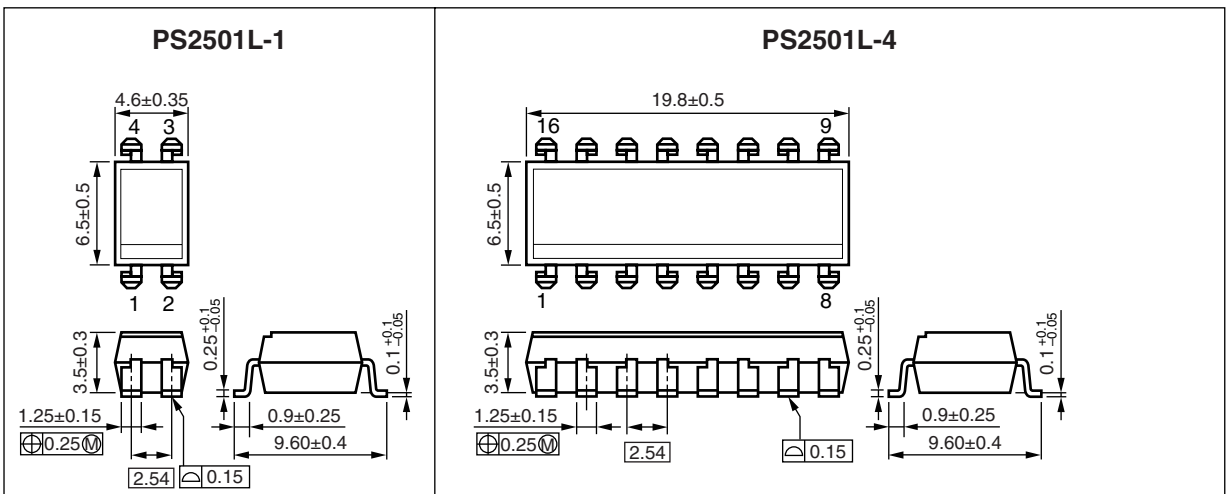
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<R> PACKAGE DIMENSIONS (UNIT : mm)

DIP Type



Lead Bending Type



PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	7 mm
Outer Creepage Distance	7 mm
Inner Creepage Distance	3.5 mm
Isolation Distance	0.3 mm

<R> MARKING EXAMPLE

**PS2501-1**

M J 0 31  
 CTR Rank Code    In-house Code    Year Assembled (Last 1 Digit)    Week Assembled

	Package	Made in Japan	Made in Taiwan
Pb-Free	New PKG	J	K
Pb-Free and Halogen Free *1	New PKG	R	Y

\*1 Special version

**PS2501-4**

N J 0 31  
 Country Assembled    In-house Code    Year Assembled (Last 1 Digit)    Week Assembled

Package	Made in Japan
New PKG	J

<R> **ORDERING INFORMATION**

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number <sup>*1</sup>
PS2501-1	PS2501-1-A	Pb-Free	Magazine case 100 pcs	Standard products (UL approved)	PS2501-1
PS2501L-1	PS2501L-1-A		Embossed Tape 2 000 pcs/reel		
PS2501L-1-F3	PS2501L-1-F3-A				
PS2501-4	PS2501-4-A		Magazine case 20 pcs		PS2501-4
PS2501L-4	PS2501L-4-A				
PS2501-1	PS2501-1Y-A	Special version	Magazine case 100 pcs	Standard products (UL approved)	PS2501-1
PS2501L-1	PS2501L-1Y-A	(Pb-Free and Halogen Free)	Embossed Tape 2 000 pcs/reel		
PS2501L-1-F3	PS2501L-1Y-F3-A				

\*1 For the application of the Safety Standard, following part number should be used.

**ABSOLUTE MAXIMUM RATINGS (Unless otherwise specified, T<sub>A</sub> = 25°C)**

Parameter		Symbol	Ratings		Unit
			PS2501-1, PS2501L-1	PS2501-4, PS2501L-4	
Diode	Reverse Voltage	V <sub>R</sub>	6		V
	Forward Current (DC)	I <sub>F</sub>	80		mA/ch
	Power Dissipation Derating	ΔP <sub>D</sub> /°C	1.5	1.2	mW/°C
	Power Dissipation	P <sub>D</sub>	150	120	mW/ch
	Peak Forward Current <sup>*1</sup>	I <sub>FP</sub>	1		A/ch
Transistor	Collector to Emitter Voltage	V <sub>CEO</sub>	80		V
	Emitter to Collector Voltage	V <sub>ECO</sub>	7		V
	Collector Current	I <sub>C</sub>	50		mA/ch
	Power Dissipation Derating	ΔP <sub>C</sub> /°C	1.5	1.2	mW/°C
	Power Dissipation	P <sub>C</sub>	150	120	mW/ch
Isolation Voltage <sup>*2</sup>		BV	5 000		Vr.m.s.
Operating Ambient Temperature		T <sub>A</sub>	-55 to +100		°C
Storage Temperature		T <sub>stg</sub>	-55 to +150		°C

\*1 PW = 100 μs, Duty Cycle = 1%

\*2 AC voltage for 1 minute at T<sub>A</sub> = 25°C, RH = 60% between input and output.

Pins 1-2 shorted together, 3-4 shorted together (PS2501-1, PS2501L-1).

Pins 1-8 shorted together, 9-16 shorted together (PS2501-4, PS2501L-4).

**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

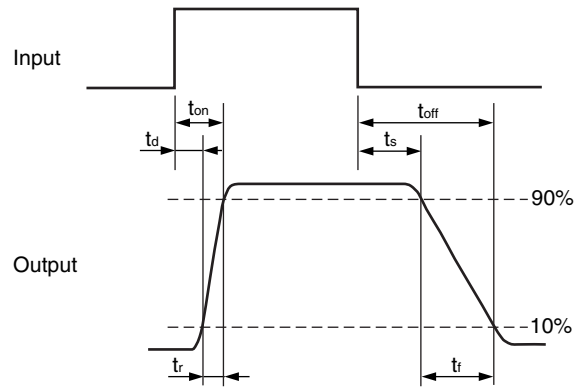
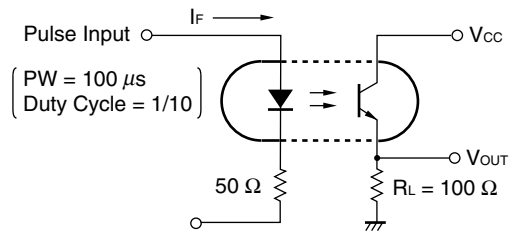
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		1.17	1.4	V
	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 5 V			5	μA
	Terminal Capacitance	C <sub>t</sub>	V = 0 V, f = 1.0 MHz		50		pF
Transistor	Collector to Emitter Dark Current	I <sub>CEO</sub>	V <sub>CE</sub> = 80 V, I <sub>F</sub> = 0 mA			100	nA
Coupled	Current Transfer Ratio (I <sub>c</sub> /I <sub>F</sub> ) <sup>*1</sup>	CTR	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	80	300	600	%
	Collector Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>F</sub> = 10 mA, I <sub>c</sub> = 2 mA			0.3	V
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1.0 kV <sub>DC</sub>	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.5		pF
	Rise Time <sup>*2</sup>	t <sub>r</sub>	V <sub>CC</sub> = 10 V, I <sub>c</sub> = 2 mA, R <sub>L</sub> = 100 Ω		3		μs
	Fall Time <sup>*2</sup>	t <sub>f</sub>			5		

\*1 CTR rank ( \* : only PS2501-1, PS2501L-1)

- K\* : 300 to 600 (%)
- L\* : 200 to 400 (%)
- M\* : 80 to 240 (%)
- D\* : 100 to 300 (%)
- H\* : 80 to 160 (%)
- W\* : 130 to 260 (%)
- Q\* : 100 to 200 (%)
- N : 80 to 600 (%)

<R>

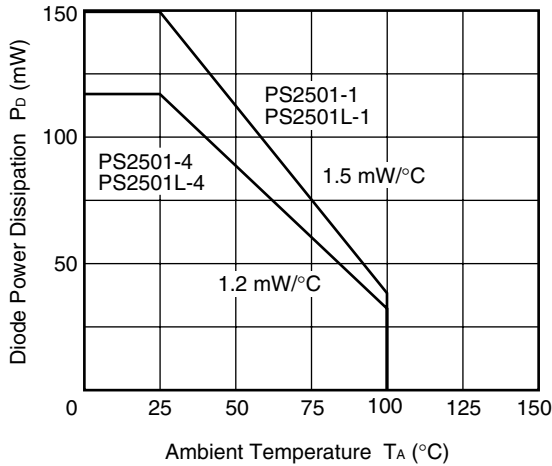
\*2 Test circuit for switching time



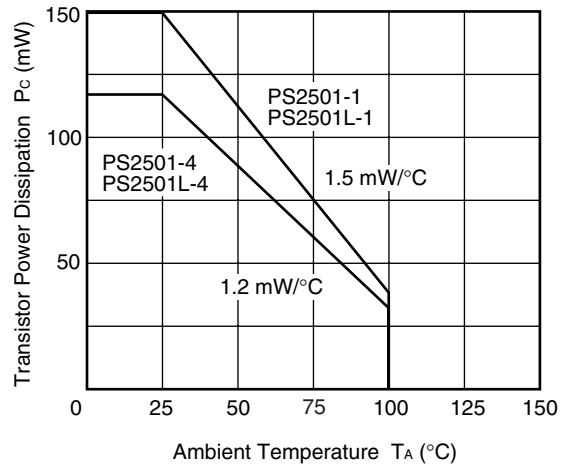


TYPICAL CHARACTERISTICS (Unless otherwise specified,  $T_A = 25^\circ\text{C}$ )

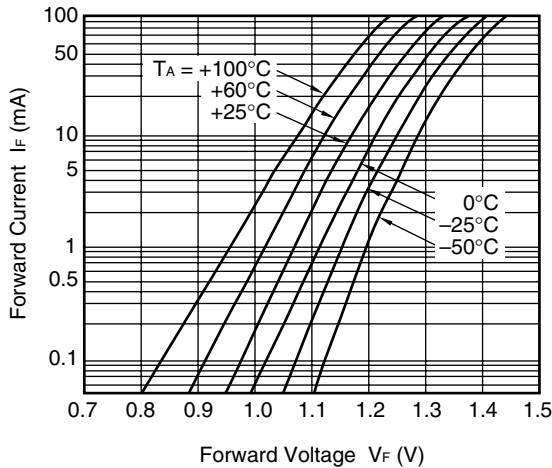
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



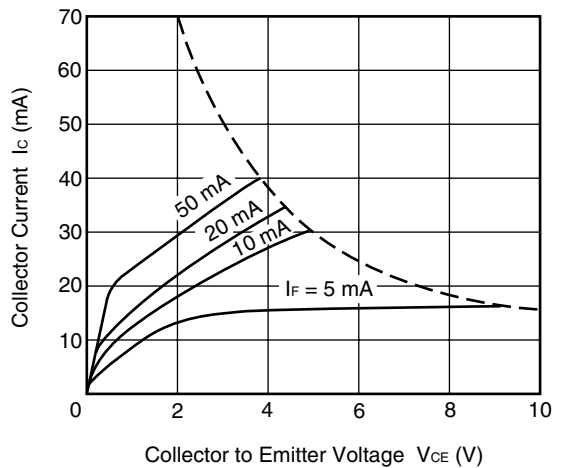
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



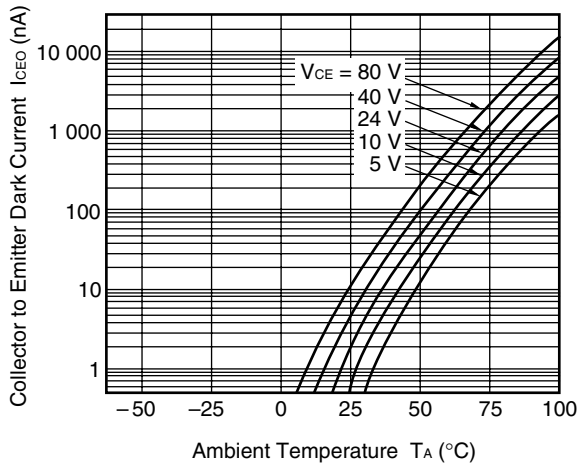
FORWARD CURRENT vs. FORWARD VOLTAGE



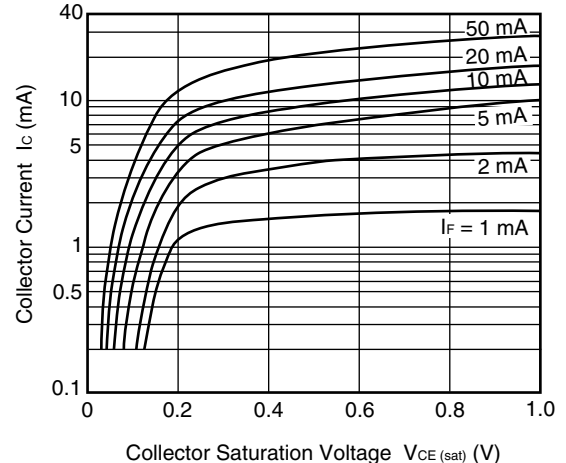
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE

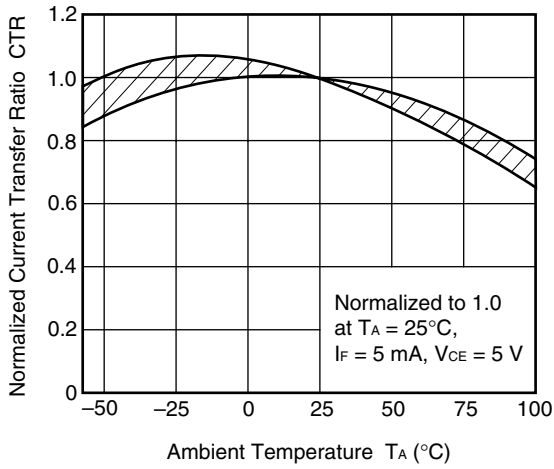


COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE

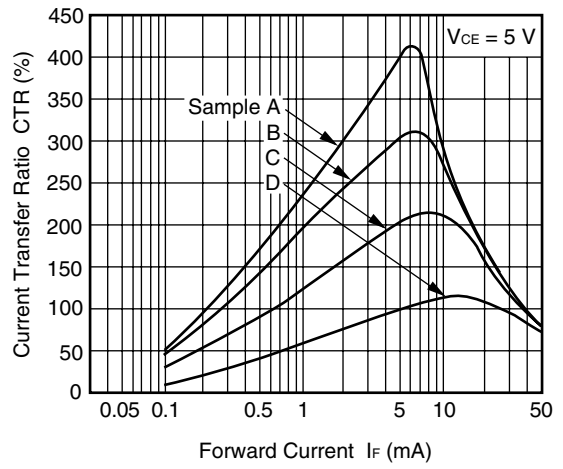


**Remark** The graphs indicate nominal characteristics.

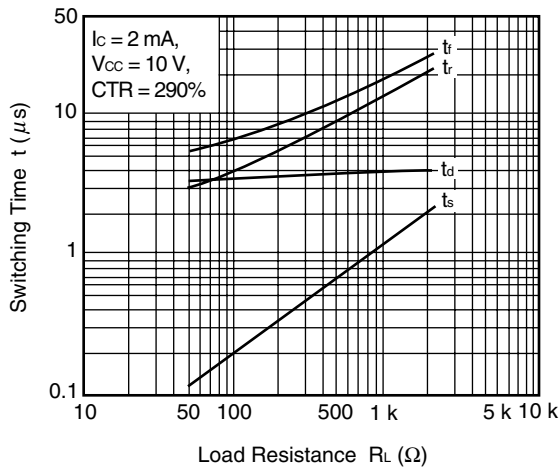
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



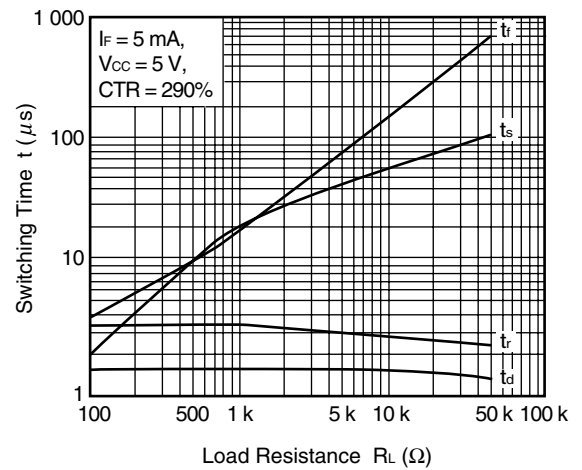
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



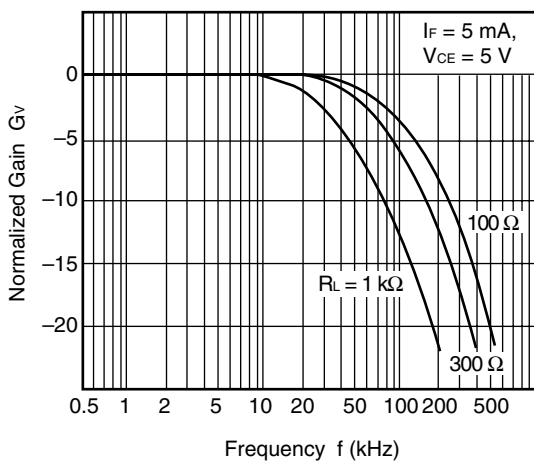
SWITCHING TIME vs. LOAD RESISTANCE



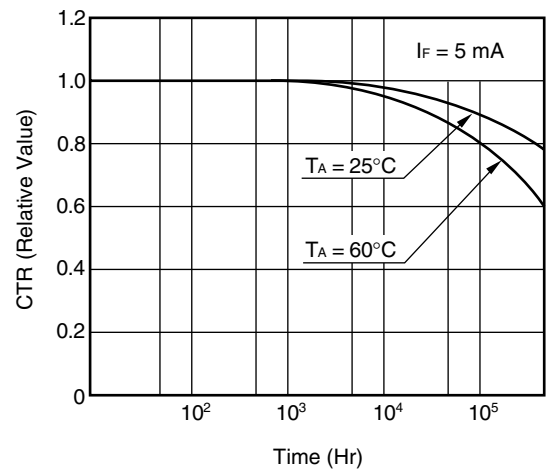
SWITCHING TIME vs. LOAD RESISTANCE



FREQUENCY RESPONSE



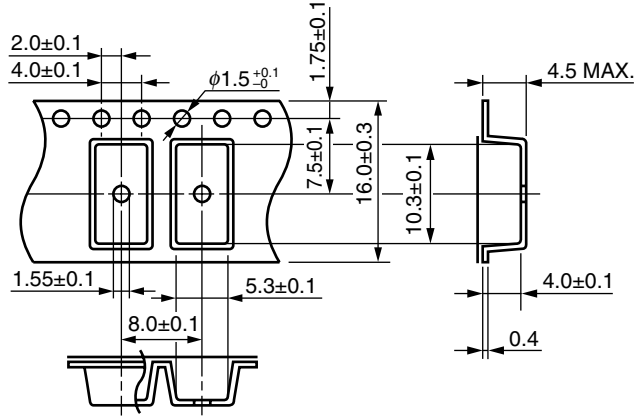
LONG TERM CTR DEGRADATION



**Remark** The graphs indicate nominal characteristics.

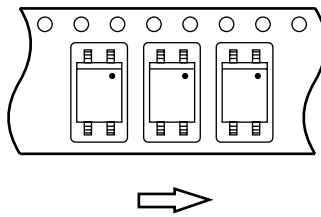
TAPING SPECIFICATIONS (UNIT : mm)

Outline and Dimensions (Tape)

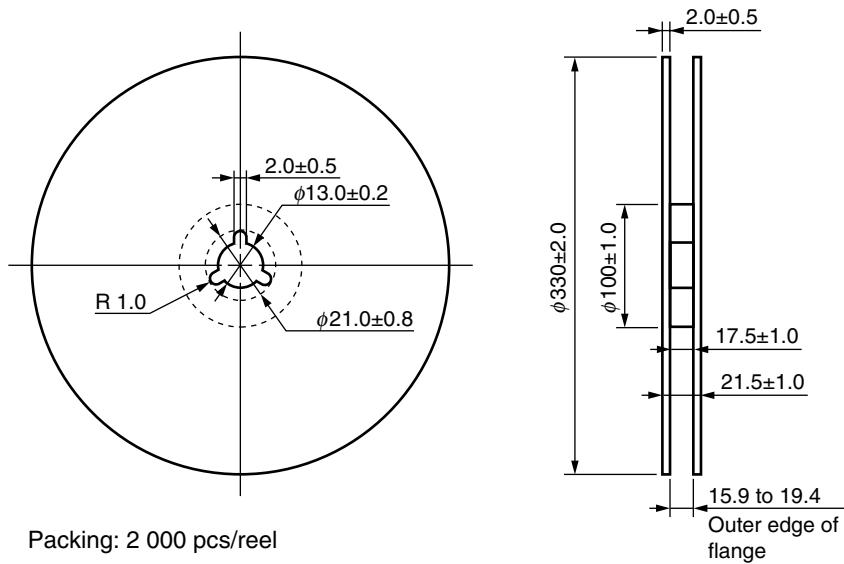


Tape Direction

PS2501L-1-F3



Outline and Dimensions (Reel)



Packing: 2 000 pcs/reel

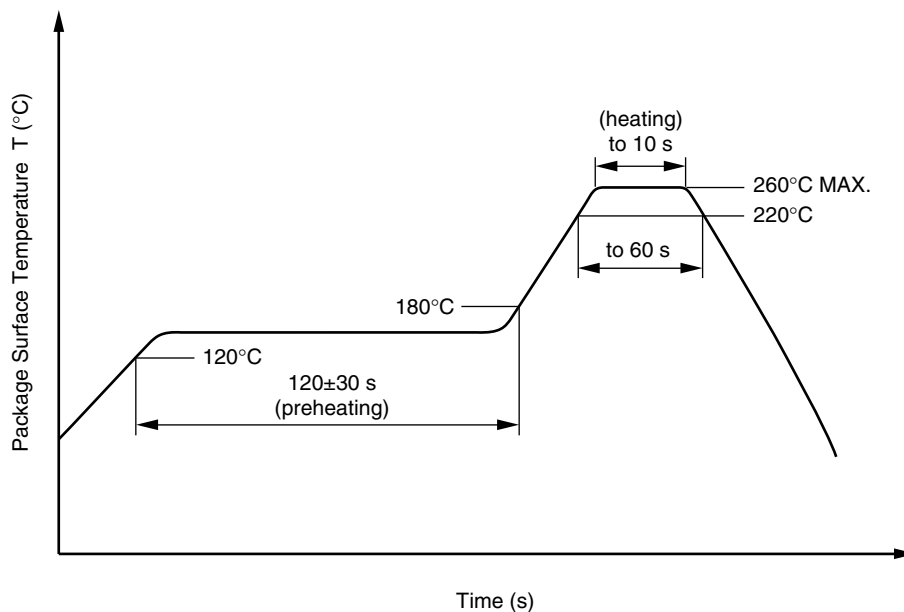
**NOTES ON HANDLING**

**1. Recommended soldering conditions**

**(1) Infrared reflow soldering**

- Peak reflow temperature 260°C or below (package surface temperature)
- Time of peak reflow temperature 10 seconds or less
- Time of temperature higher than 220°C 60 seconds or less
- Time to preheat temperature from 120 to 180°C 120±30 s
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



**(2) Wave soldering**

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

**(3) Soldering by soldering iron**

- Peak temperature (lead part temperature) 350°C or below
- Time (each pins) 3 seconds or less
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

**(4) Cautions**

- Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

**2. Cautions regarding noise**

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

**3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler**

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. This tendency may sometimes be obvious, especially below  $I_F = 1$  mA.

Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

**USAGE CAUTIONS**

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.

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